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.[VFDSTR] *****
.[VFDSTR] *****
Rescue Install_Key
POWER button
[HDMI]: Set I2C Speed = 50 kHz
[HDMI]: Chk_HotPlug() TV is not connected.
[HDMI]: Read_EDID abort, HDMI cable plug off.
[HDMI]: TV is not connected.
2nd for PAL
PAL logo
[VFDSTR] UPGRADE
Rescue kernel in FLASH, total size = 0x1f52d0
seg.1 addr 0xa0a04b0b, size = 0x1f52d0
seg.2 addr 0x0, size = 0x0
copy rescue0: from a0a04b0b to 81000000, len = 1f52d0
decompressing rescue: from 81000000 to 80100000: size = 1f52d0
lzma finished
decompressing rescue done
go 0x80100000
defer 625p logo
Reset Ethernet Mac.

Address = 0x80100000
Linux version 2.6.34-VENUS (william@rtdmpsa-05) (gcc version 4.3.6 20110218 (pre
release) (GCC) ) #21 PREEMPT Wed Dec 4 13:08:14 CST 2013

Realtek LINUX started...
Venus setting:
    ROSs have 4194304 bytes RAM.
    System CPU has 1 UART.
    System CPU uses internal timer interrupt.
    Bootloader version: KIIE.0402.1067. This version string is of new format
.
    The information of this board: Company ID:0x0    CPU ID: 0x4    Board ID
: 0x402
    Ethernet Mac address: 00.CE.39.B9.EA.1C
    Model Config length=3
Base year of RTC is 2010.
*****
You didn't specify audio image address, use default instead...
*****
audio addr: 0
Config serial console: console=ttyS0,115200n8r
bootconsole [early0] enabled
CPU revision is: 00019385 (MIPS 24Kc)
FPU revision is: 01739300
Determined physical RAM map:
memory: 00020000 @ 00000000 (reserved)
memory: 000e0000 @ 00020000 (ROM data)
memory: 01b00000 @ 00100000 (usable)
memory: 00400000 @ 01c00000 (reserved)
memory: 0e000000 @ 02000000 (usable)
memory: 00100000 @ 10000000 (reserved)
memory: 07f00000 @ 10100000 (usable)
memory: 00100000 @ 18000000 (reserved)
memory: 05b00000 @ 18100000 (usable)
Wasting 8192 bytes for tracking 256 unused pages
Initrd not found or empty - disabling initrd
Zone PFN ranges:
    DMA      0x00000000 -> 0x00002000
    Normal   0x00002000 -> 0x0001dc00
Movable zone start PFN for each node
Node 0: 64512
early_node_map[1] active PFN ranges
0: 0x00000000 -> 0x0001dc00
zone DMA      size: 2000      realsize: 2000
zone Normal   size: dc00      realsize: dc00
zone Movable  size: e000      realsize: e000
zonelist: Movable Normal DMA
Built 1 zonelists in Zone order, mobility grouping off. Total pages: 121856
Kernel command line: console=ttyS0,115200n8r ip=192.168.0.9::192.168.0.254:255.
0.0.0:::
PID hash table entries: 2048 (order: 1, 8192 bytes)
Dentry cache hash table entries: 65536 (order: 6, 262144 bytes)
Inode-cache hash table entries: 32768 (order: 5, 131072 bytes)
Primary instruction cache 32kB, VIPT, 4-way, linesize 32 bytes.
Primary data cache 64kB, 4-way, VIPT, no aliases, linesize 32 bytes
Writing ErrCtl register=0000c00f

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Readback ErrCtl register=0000c00f
Memory: 468824k/480256k available (3506k kernel code, 11432k reserved, 852k data
, 2600k init, 0k highmem)
Hierarchical RCU implementation.
NR_IRQS:8
Estimated CPU frequency: 796.54 MHz
Console: colour dummy device 80x25
Calibrating delay loop... 529.20 BogoMIPS (lpj=2646016)
Mount-cache hash table entries: 512
devtmpfs: initialized
NET: Registered protocol family 16
prom_flashsize = 0x80000000
Flash size is 0x2000000
bio: create slab <bio-0> at 0
SCSI subsystem initialized
usbcore: registered new interface driver usbfs
usbcore: registered new interface driver hub
usbcore: registered new device driver usb
cfg80211: Calling CRDA to update world regulatory domain
Switching to clocksource MIPS-DMP
NET: Registered protocol family 2
IP route cache hash table entries: 4096 (order: 2, 16384 bytes)
Switched to NOHz mode on CPU #0
TCP established hash table entries: 16384 (order: 5, 131072 bytes)
TCP bind hash table entries: 16384 (order: 4, 65536 bytes)
TCP: Hash tables configured (established 16384 bind 16384)
TCP reno registered
UDP hash table entries: 256 (order: 0, 4096 bytes)
UDP-Lite hash table entries: 256 (order: 0, 4096 bytes)
NET: Registered protocol family 1
RPC: Registered udp transport module.
RPC: Registered tcp transport module.
RPC: Registered tcp NFSv4.1 backchannel transport module.
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```
*****          se init module          *****
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se init module major number = 253
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```
*****          md init module          *****
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md init module major number = 252
size of RPC_POLL_Dev 52 and RPC_INTR_Dev 52...
init vdso successfull
rtktlb: Hello, Realtek TLB Mapper...
AllocBP size: 688128 order: 8 offset: 0
<<<DVR FREE AREA>>>
0:      0
1:      0
2:      0
3:      0
4:      0
5:      0
6:      0
7:      0
8:      0
9:      0
10:     0
11:     0
12:     0
13:     7
      Addr: 82000000, order: 13
      Addr: 84000000, order: 13
      Addr: 86000000, order: 13
      Addr: 88000000, order: 13
      Addr: 8a000000, order: 13
      Addr: 8c000000, order: 13
      Addr: 8e000000, order: 13
NTFS driver 2.1.29 [Flags: R/O].
msgmni has been set to 915
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alg: No test for stdrng (krng)
io scheduler noop registered
io scheduler deadline registered
io scheduler cfq registered (default)
Serial: 8250/16550 driver, 2 ports, IRQ sharing enabled
serial8250: ttyS0 at MMIO 0x0 (irq = 3) is a 16550A
console [ttyS0] enabled, bootconsole disabled
console [ttyS0] enabled, bootconsole disabled
serial8250: ttyS1 at MMIO 0x0 (irq = 3) is a 16550A
brd: module loaded
loop: module loaded
Realtek Bulid-In SATA Driver Rev: SATURN (2011-02-24 15:10)
sata_rtk sata_rtk: version 0.01
sata_rtk sata_rtk: slots 32 ports 1
scsi0 : sata_rtk
ata1: SATA max UDMA/133 irq 4
BGA Package Realtek NAND Flash Driver Rev: 337905 (2010-10-18)
(X) Enable interrupt mechanism of NAND driver.
Get parameter from register...
[rtk_nand_init] Bootcode size 0x3200000
[rtk_nand_init] Factory parameter size 0x400000
[rtk_nand_init] Disable NFCP...
nand_base_rtk version: 0824-2011
(X) NAND_POWEROFF_CARDREADER_WITH_MULTI_READ
(X) NAND_READ_SKIP_UPDATE_BBT
READ ID: 0xad 0xd5 0x94 0x9a 0x74 0x42
One H27UAG8T2B chip has 1 die(s) on board
nand part=H27UAG8T2B, id=add5949a, device_size=2147483648, chip_size=2147483648,
 num_chips=1, page_size=8192, isLastPage=1, eccBits=24
[rtk_nand_scan_bbt] have created bbt B1, just loads it !!
[dump_BBT] Nand BBT Content
[0] (0, 526, 0, 1023)
[1] (0, 514, 0, 1022)
RTK: using the whole nand as a partition
[rtk_nand_init] Ecc bit select 24
Realtek Nand Flash Driver is successfully installing.
VenusSFC MTD init...
NOR flash support list ..
(X) HWSEMA support.
(V) AUTO HW POLL support.
(X) 4Bytes mode support.
(V) Max erase size support.
(V) Sfc_delay support.
[venus_sfc_init] FLASH_BASE: 0xbdc00000
[venus_sfc_init] Flash size: 0x2000000
Read id original value : 0xc22015c2
--RDID Seq: 0xc2 | 0x20 | 0x15
VenusSFC MTD: MXIC MX25L1605 detected.
SFC_4KB_ERASE
VenusSFC MTD: Enable VenusSFC MD PP callback function.
MD_READ Enable..
MD_WRITE Enable..
[venus_sfc_init] descriptor size 0x2000000
RedBoot partition parsing not available
Venus SFC: using single partition Venus SFC: (for SST/SPANSION/MXIC SPI Flash)
this Jupiter eth RX_OFFSET = 0x0
8139cplus: 10/100 PCI Ethernet driver v1.2 (Mar 22, 2004)
MAC address = 0x00.CE.39.B9.EA.1C
eth0: RTL-8139C+ at 0xb8016000, 00:ce:39:b9:ea:1c, IRQ 2
ehci_hcd: USB 2.0 'Enhanced' Host Controller (EHCI) Driver
[usb_param] setting for saturn
## cfyeh-debug USBPHY_Register_Setting(861) 0xb8013808 = 0x4400a081
## cfyeh-debug USBPHY_Register_Setting(863) 0xb8013808 = 0x4400a080
## cfyeh-debug USBPHY_SetReg_Default_33_port(210) port 1 dr 7
## cfyeh-debug USBPHY_SetReg_Default_33_port(210) port 2 dr 7
## [cfyeh-debug] USBPHY_Register_Setting(933)
## [cfyeh-debug] USBPHY_Register_Setting(935)
## [cfyeh-debug] USBPHY_Register_Setting(937)
## cfyeh-debug USBPHY_SetReg_Default_39(289) tx_delay 2
[usb1_param] usbphy reg 32, set sh = 0xa, get sh = 0xa, 4bit
[usb1_param] usbphy reg 35, set src = 0x7, get src = 0x7, 3bit
[usb1_param] usbphy reg 36, set senh = 0xa, get senh = 0xa, 4bit
```